## S ( V I S THE

## **Shree H.N.Shukla group of colleges**

## **PHYSICS**

T.Y.B.Sc. (Sem. V) (CBCS) Unit Test examination PAPER- 503

## **UNIT 3,4 - POWER SUPPLY AND IC**

	ONIT 5,4 TOWER SOTTET AND IC		
DATE :	Total marks : 30		
Instructi	ons: All questions are compulsory.		
The right side figure indicates total marks of the question.			
	Draw the figure wherever necessary.		
	Write answers of all the questions in main answer	sheets.	
	SECTION-A		
Q.1: One	e marks questions:	[5 MARKS]	
1	Full form for CMRR		
2	The ideal bandwidth of OP-AMP is		
3	A zener diode regulator has law effifciency for	·	
4	What is op-amp?		
5	Define : types of IC.		
	SECTION – B		
Q.2 (A):	Short Questions: Write any three: [2 Marks each]	[6 MARKS]	
1	Explai CMRR.		
2	Explain the scale integration of IC.		
3	Define two or three advantage of IC.		
Q.2 (B) :	Short questions: Write all three: [3 Marks each]	[9 MARKS]	
1	Explain op-amp as a volatge comparator.		
2	Explain transisotr series voltage regulator.		
3	Explain op-amp as an Unity follower.		
Q.2 (C):	Write Detail Note on [Any two]: [5 Marks each]	[10 MARKS	
1	Explain op-amp as diffrenetiator .		
2	Explain op-amp as an adder.		

Explain transistor shunt voltage regulator.

Explain fabrication of monolithic IC.

3

4